

10/030687

FILED UNDER 35 U.S.C. 371

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10030687	FILING DATE 01/14/2002	CLASS 327	SUBCLASS	GAU 2816	EXAMINER <i>m. nuyen</i>
**APPLICANT'S: Nakanishi Jingo;					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A 371 OF PCT/JP00/03638 06/05/2000 <i>m yes</i>					
BEST AVAILABLE COPY					
**FOREIGN APPLICATIONS VERIFIED: <i>m NO</i>					
PG-PUB DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/>		yes <input checked="" type="checkbox"/> no <input type="checkbox"/>		ATTORNEY DOCKET NO	
35 USC 110 conditions met <input type="checkbox"/>		yes <input checked="" type="checkbox"/> no <input type="checkbox"/>		57454-315	
Verified and Acknowledged Examiners's initials <i>bb</i>					
TITLE : Clock synchronizer					
U.S. DEPT. OF COM. / PAT. & TM-PTO-4361 (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner		CLAIMS ALLOWED	
				Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner		DRAWING	
Amount Due	Date Paid			Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE		Application Examiner	
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